

Bachelor in Informatics and Communications School School of Public Management, Communication and Tourism Academic Year 2023/2024 Year of study 1 Level 1-1 ECTS credits 6.0 Type Semestral Semester 2 Code 9188-320-1102-00-23 Workload (hours) 162 Contact hours T 15 TP - PL 45 TC - S - E - OT 20 O - T - Lectures; TP - Lectures and problem-solving, PL - Problem-solving, project or laboratory; TC - Fieldwork; S - Seminar; E - Placement; OT - Tutorial; O - Other	Course Unit	Computer System Architecture Informatics and Communications			Field of study	Network and Computer Systems	
Type Semestral Semester 2 Code 9188-320-1102-00-23 Workload (hours) 162 Contact hours T 15 TP - PL 45 TC - S - E - OT 20 O -	Bachelor in				School	School of Public Management, Communication and Tourism	
Workload (hours) 162 Contact hours T 15 TP - PL 45 TC - S - E - OT 20 O -	Academic Year	2023/2024	Year of study	1	Level	1-1	ECTS credits 6.0
	Туре	Semestral	Semester	2	Code	9188-320-1102-00-23	
1 - Lectures, 17 - Lectures and protein-solving, PL - Problem-solving, project of factoratory, 10 - Pietowork, 5 - Seminar, E - Pracement, 01 - Tutoriar, 0 - Other							

Name(s) of lecturer(s) Luis Fatima Goncalves Liberal

Learning outcomes and competences

At the end of the course unit the learner is expected to be able to:

- . Identify and describe the various hardware components of a common computer system . Install and configure hardware to a common computer system.
- 3. Identify the importance of configuration, maintenance and optimization of a basic operating system in a user's perspective.

 4. Install, configure and optimize the operation of operating systems (Windows, Linux) on a user's perspective.

 5. Demonstrate the ability to work in various operating systems.

Prerequisites

Before the course unit the learner is expected to be able to: Without prerequisites.

Course contents

Computer hardware, assembling a PC, BIOS setup, diagnosis of errors and software.

Course contents (extended version)

- Computer
 The Microprocessor
 Bus and Ports
- 3. Bus and P 4. Memories
- Storage Units
- 6. Computer Assembling and Maintenance 7. BIOS Setup
- 8. Error Diagnosis and Resolution
- 9. Monitors 10. Display Adapters
- 11. Software

Recommended reading

- Magalhães, J., Gouveia, A., (2019). Hardware tecnologias e soluções. FCA. ISBN: 9789727228928.
 Branco, A., (2015). Manual de Instalação e Reparação de Computadores. 3ª Edição. FCA. ISBN: 9789727228089.
 Delgado, J., Ribeiro, C. (2014). Arquitectura de computadores. (5ª ed.). Lisboa: FCA. ISBN: 978-972-722-789-1
 Hennessy, J. L., Patterson, D. A., (2011) Computer Architecture: A Quantitative Approach. Morgan Kaufmann. Fifth Edition. ISBN: 978-0123838728
 Harris, D., Harris, D., (2012) Digital Design and Computer Architecture, Second Edition. (2 edition), Morgan Kaufmann. ISBN: 0123944244

Teaching and learning methods

For each topic of the program will be an introduction to the theory that using the expository method using a video projector. Will also apply the method interrogative, questioning systematically pupils themselves to develop the capacity of reasoning.

Assessment methods

- 1. Continuous evaluation (Regular, Student Worker) (Final)
 Intermediate Written Test 40% (Two written test. Minimum grade: 7. 0 values.)
 Practical Work 60% (Includes the completion of two projects. Minimum grade: 7. 0 values.)

 2. Final evaluation (Regular, Student Worker) (Supplementary, Special)
 Final Written Exam 40% (Written test. Minimum grade: 7. 0 values.)
 Practical Work 60% (Includes the completion of two projects. Minimum grade: 7. 0 values.)

Language of instruction

Portuguese, with additional English support for foreign students.

Electronic validation

Licotroffic validation				
Luis Fatima Goncalves Liberal	Elisabete da Anunciacao Paulo Morais	Anabela Neves Alves de Pinho	Luisa Margarida Barata Lopes	
08-05-2024	08-05-2024	09-05-2024	15-05-2024	